

Multiterabit Switch Fabrics Enabled by Proximity Communication

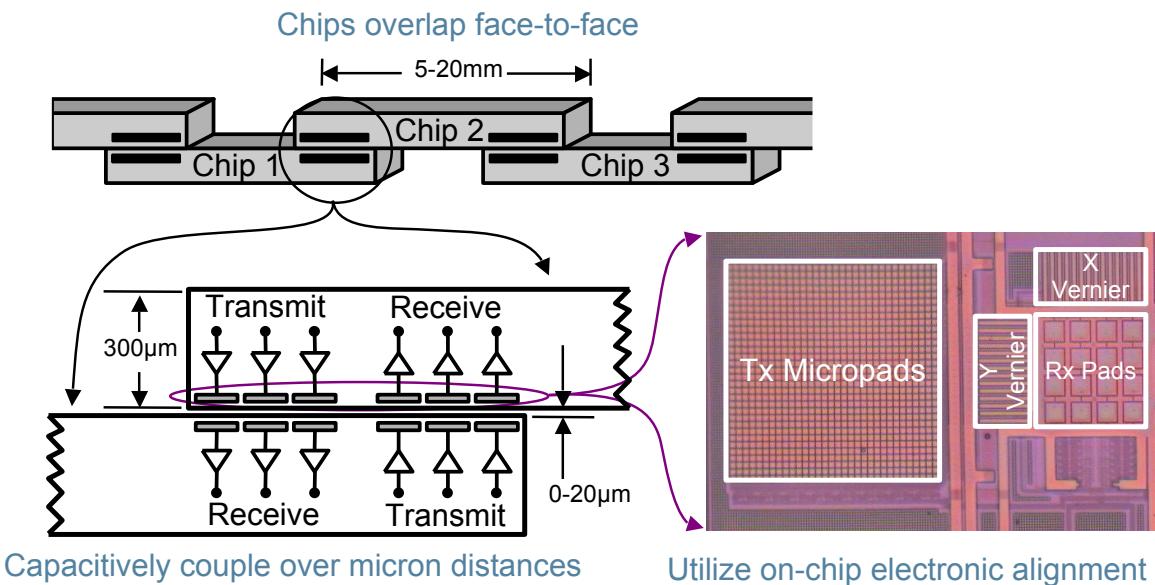
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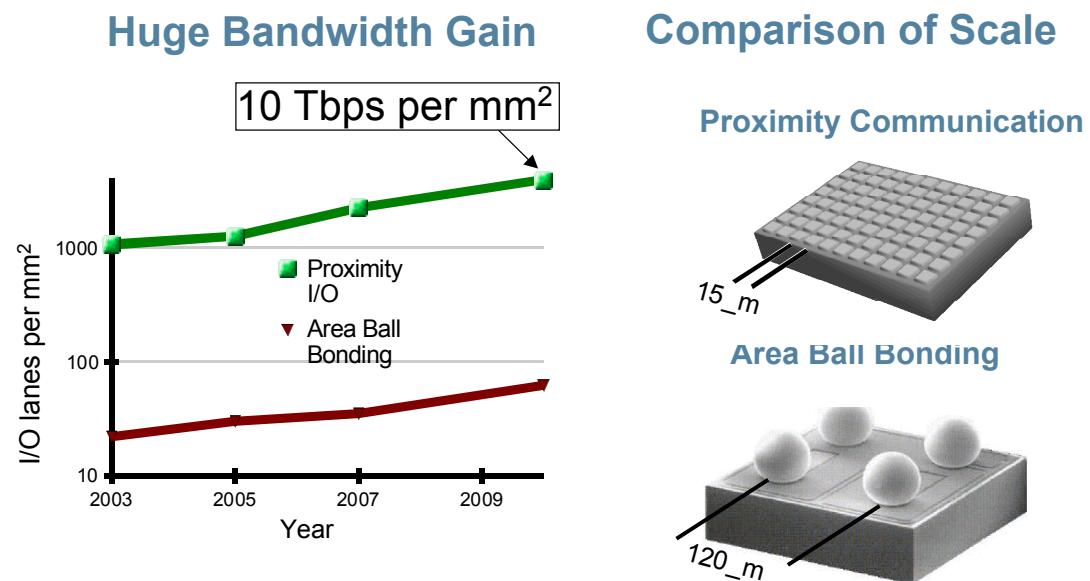
Future Interconnect Needs

- The interconnect becomes an increasingly critical system component
 - > Fatter compute nodes
 - > Increasing disparity between local and remote communication
- Data center trends
 - > Server consolidation
 - > Network consolidation
 - > Virtualization
 - > Clustering
 - > Horizontal scale beyond the chassis

Proximity Communication (PxC)



Removing the Chip IO Bottleneck

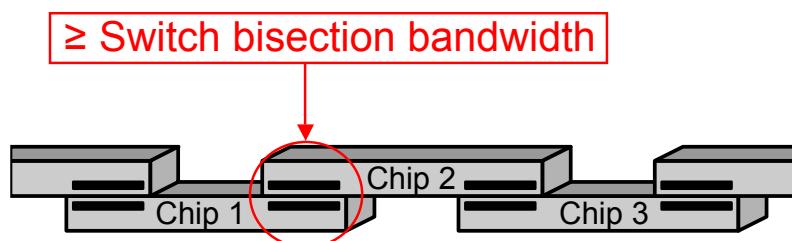


Proximity Communication Advantages

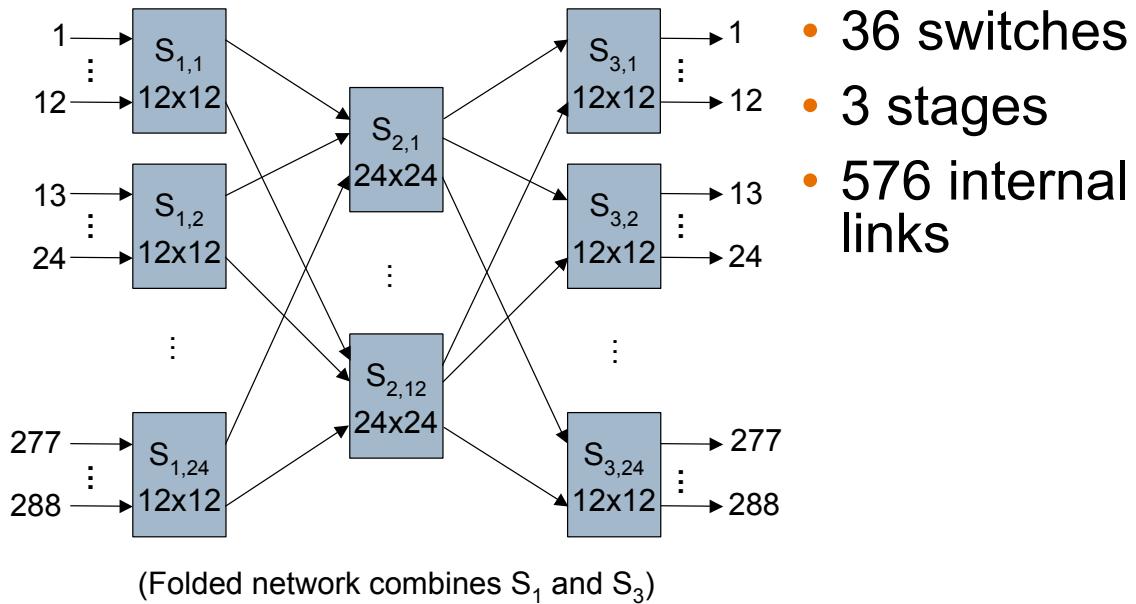
- Increases bandwidth/area
- Avoids off-chip wires
- Obviates ESD protection
- Shrinks transceiver circuits
- Lowers power consumption
- Makes multi-chip modules reworkable
- Enables smaller chips

Opportunity

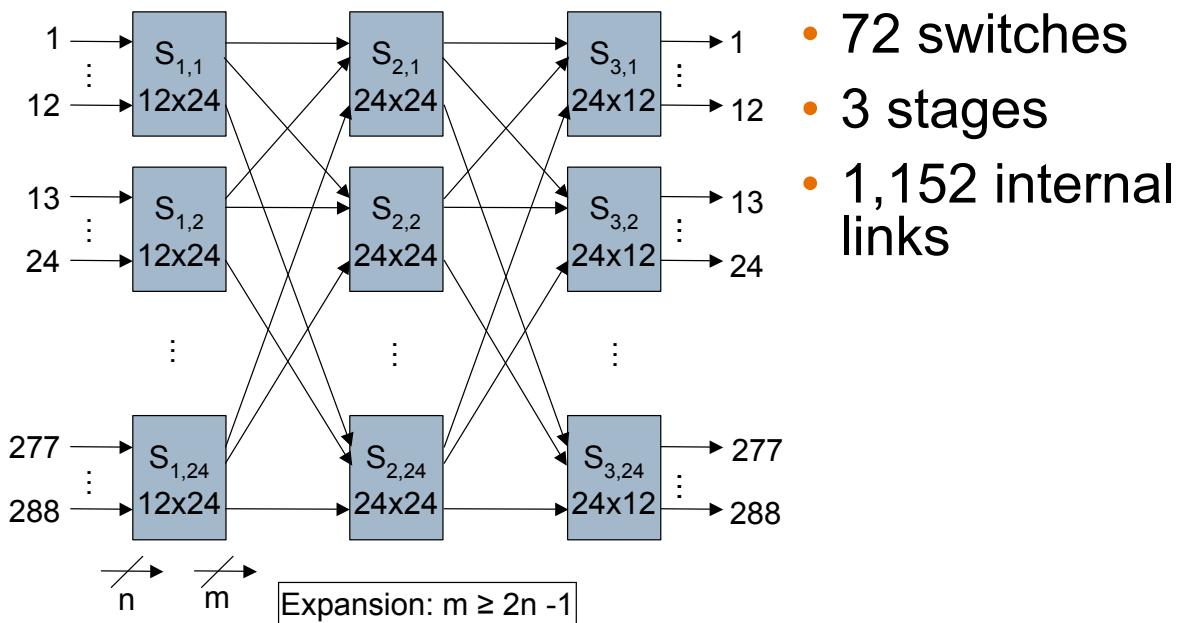
- Proximity Communication allows for building switch fabrics that scale to thousands of ports and multiple Tbps throughput using a *flat single-stage* network rather than a hierarchical multi-stage network



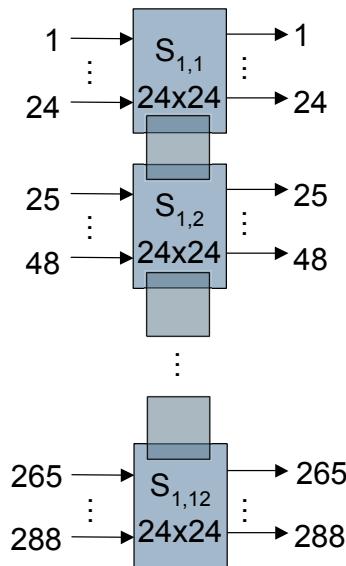
Blocking Multi-stage Switch



Non-blocking Multi-stage Switch

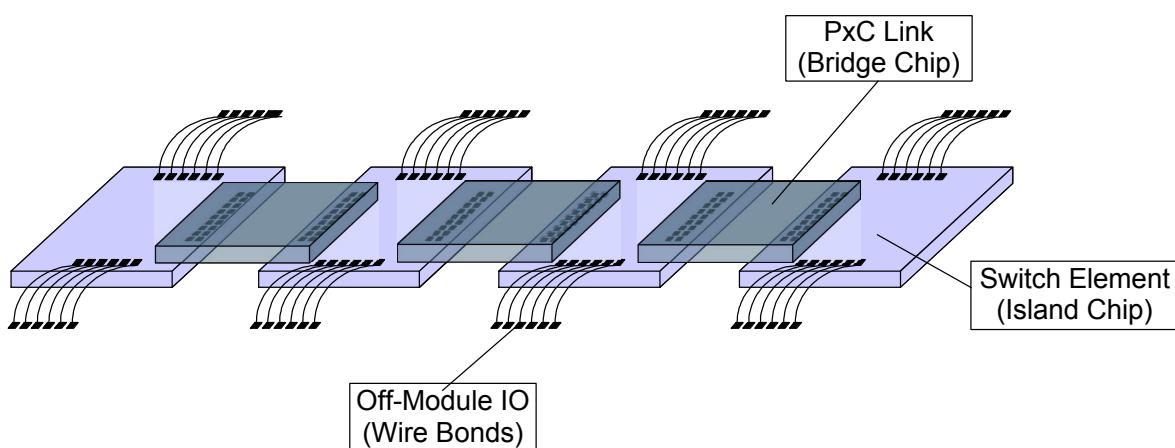


Proximity Communication Switch

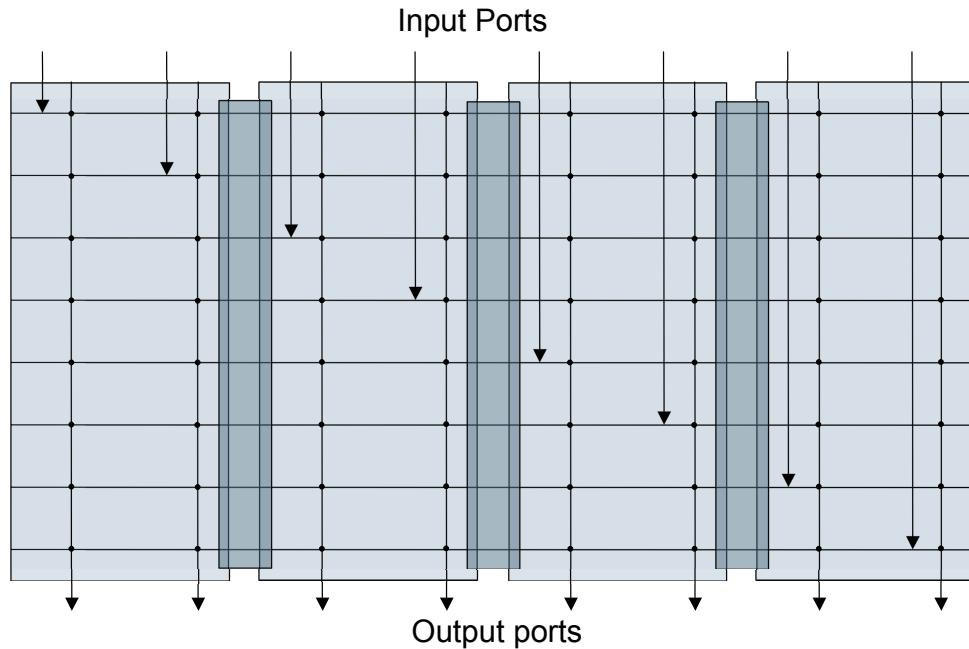


- 12 switches
- 1 stage
- PxC links

Vector Multi-Chip Module



Port-Sliced Crossbar Switch



Single-Stage PxC Switch Advantages

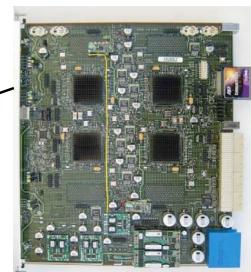
- Low deterministic latency
- Simple global scheduling
 - > No internal blocking
 - > No out-of-sequence delivery
 - > Service guarantees possible
- Lower cost
 - > Fewer switch elements
 - > Less internal wiring
- Less power
- Higher reliability

Switch Prototype Characteristics

- System characteristics
 - > 4 x 10GE ports
 - > Layer2 switching
 - > Based on ATCA standard
 - > Off-the-shelf line cards
 - > Proprietary switch blade
- Switch fabric
 - > "Vector switch" with 4 Island chips + 2 Bridge chips (3 PxC links)
 - > Off-chip connections through wire bonds

Switch Prototype

Switch Motherboard



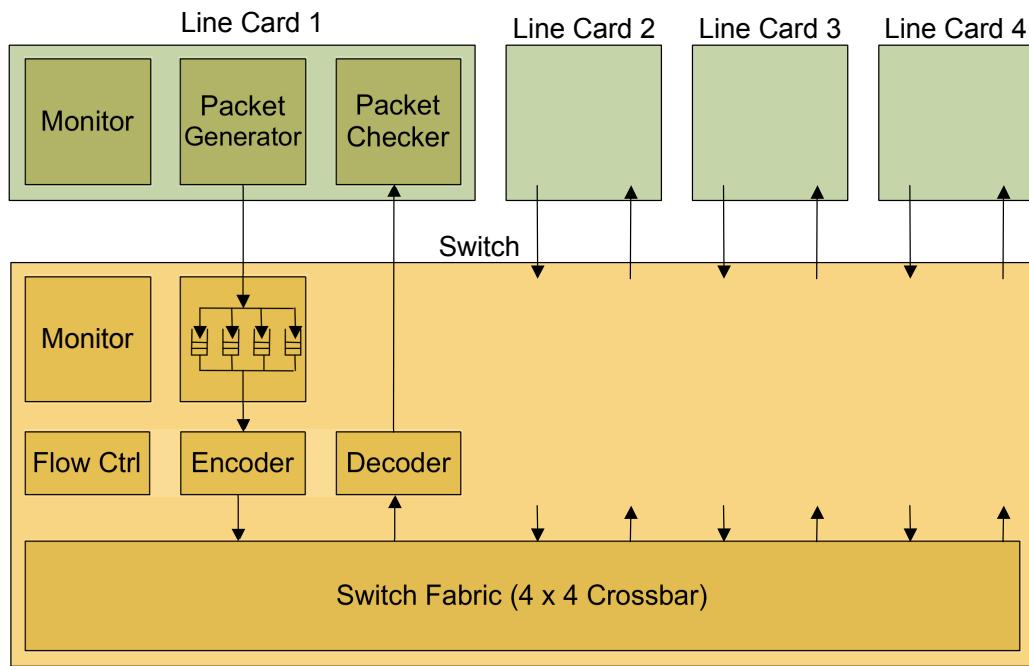
Line Card



Switch Daughtercard



Switch Prototype Organization

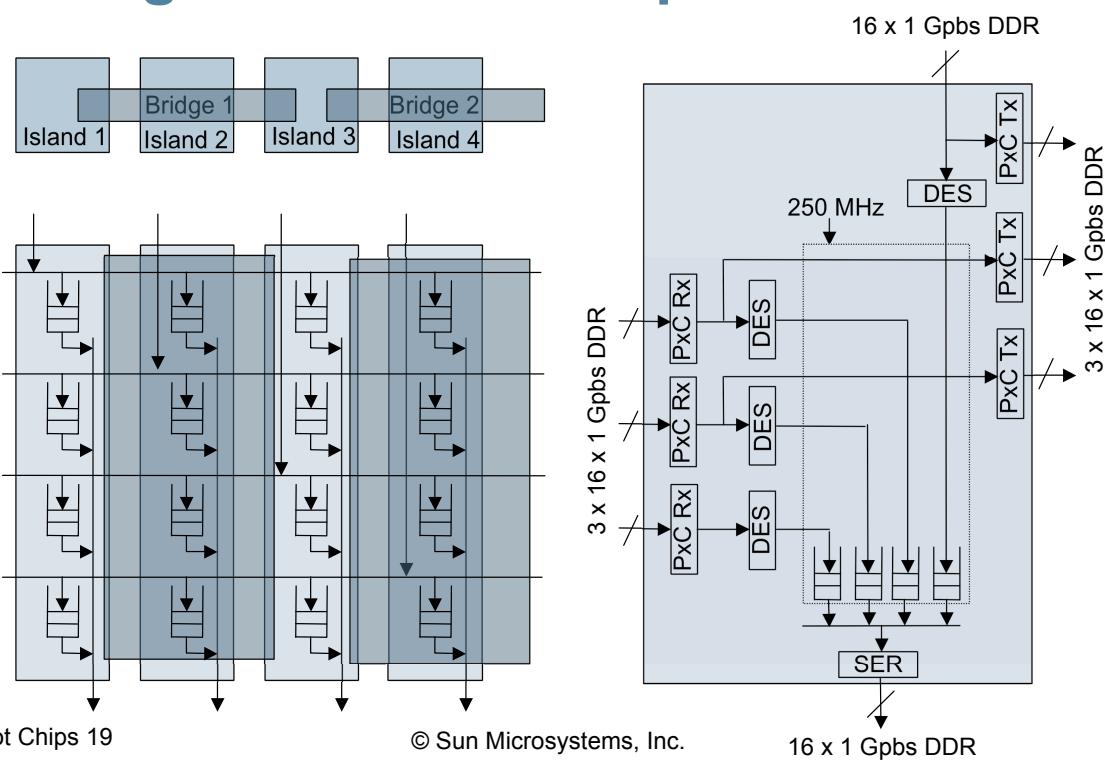


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Bridge and Island Chips

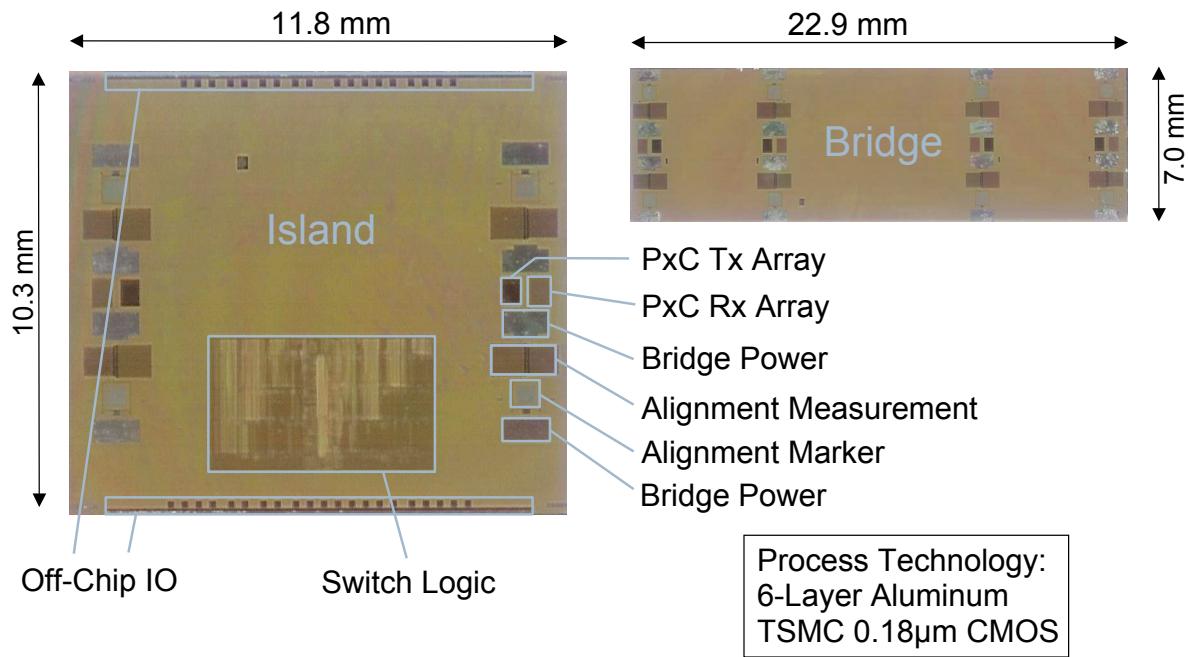


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Bridge and Island Chips

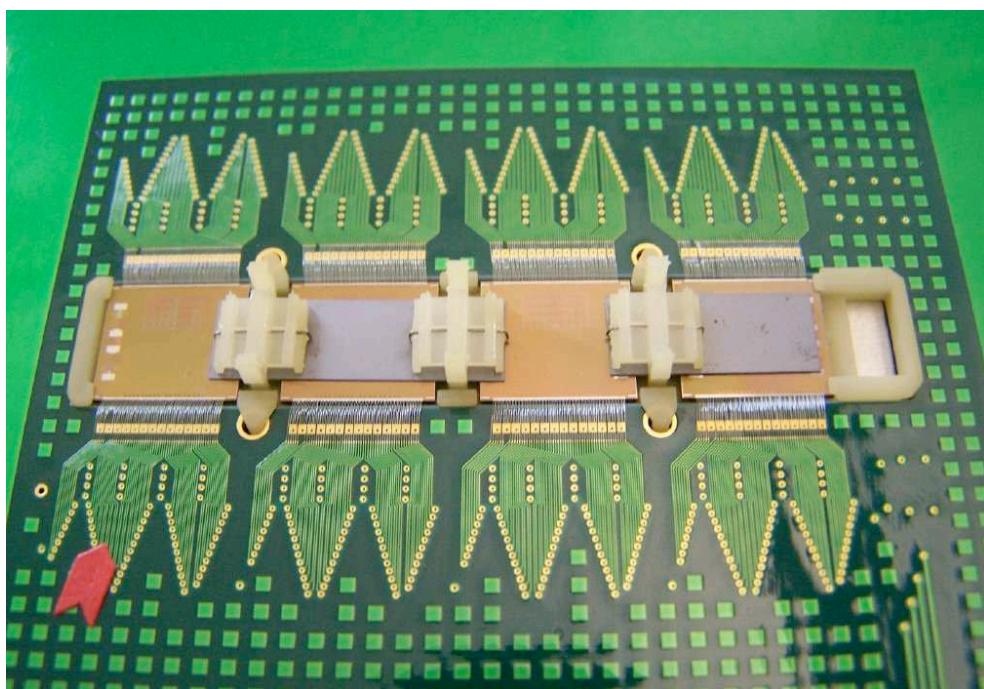


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Vector Switch Prototype

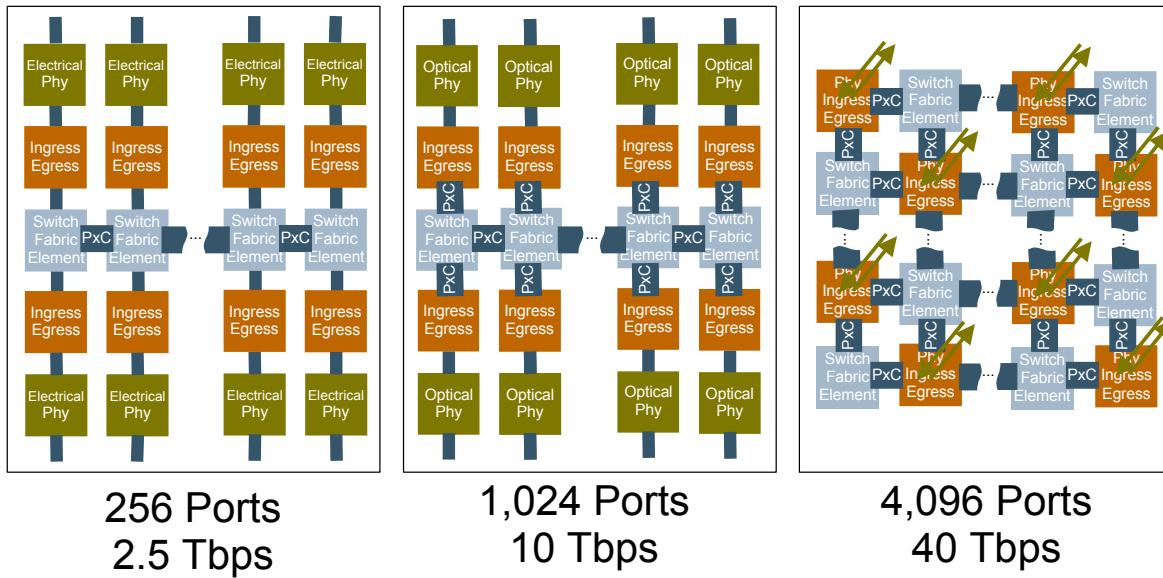


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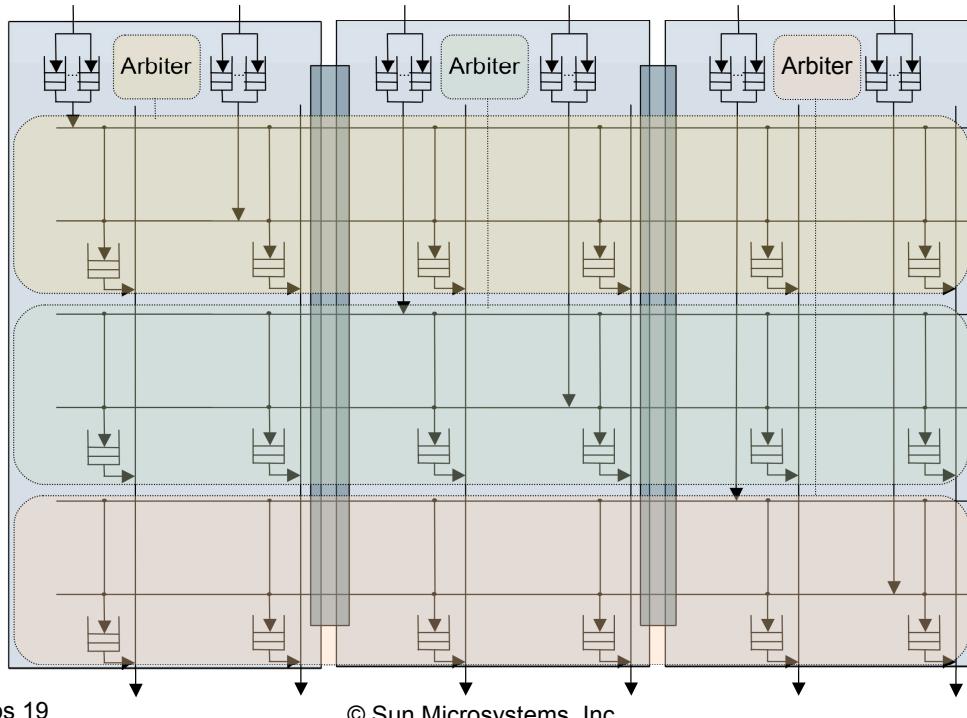
Scaling Up



Scalable Switch Architecture

- "Output Buffered Switch with Input Groups"
 - > Reduces memory requirements from $O(n^2)$ to $O(n \cdot \# \text{ Island Chips})$
 - > To be presented at Globecom 2007
- "Parallel Wrapped Wave Front Arbiter"
 - > Increases throughput of $n \times n$ Wrapped Wave Front Arbiter by a factor of n
 - > Presented at HPSR 2007

Output Buffered Switch with Input Groups



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Applications

- Data center backbone
- Blade system interconnect
- ATCA chassis aggregation
- Cluster interconnect
- System interconnect

Summary

- Proximity Communication allows for building a *flat single-stage* switch fabric that scales to thousands of ports and multiple Tbps throughput
 - > Low latency
 - > High efficiency
 - > Service guarantees
 - > Low power
 - > High physical density

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