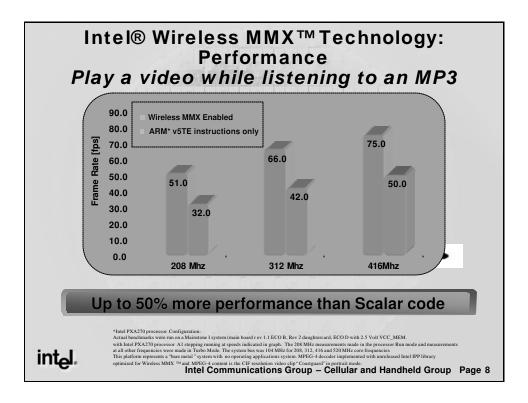
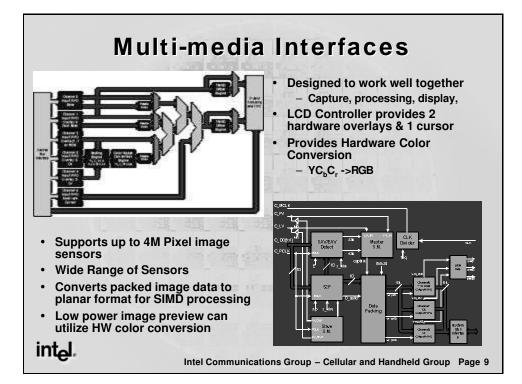
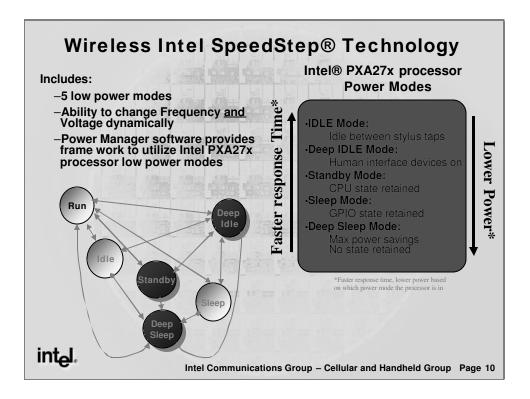
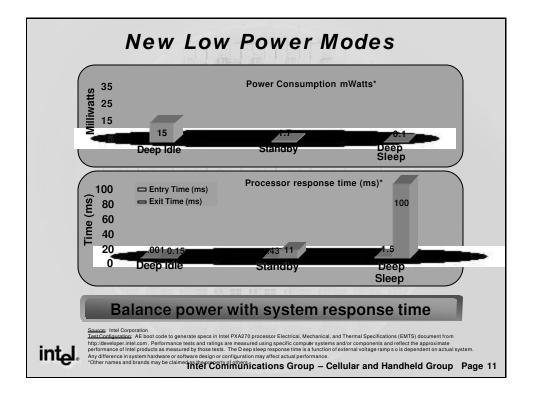


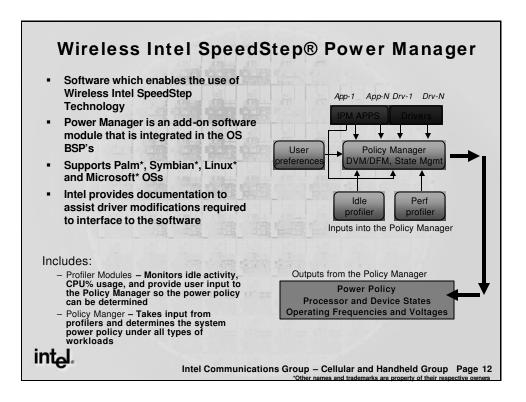
| Pipeline Organization The Intel® Wireless MMX TM unit is tightly coupled with Intel XScale® core and also contains three pipelines. | |
|---|--|
| Intel® XScale® Pipeline | F1-F2: Instruction fetch ID: Instruction decode RF: Operand access & bypass X1: -XWB: Pipe stages for ALU M1-MWB: Pipe stages for MAC D1-DWB: Pipe stages for load instructions |
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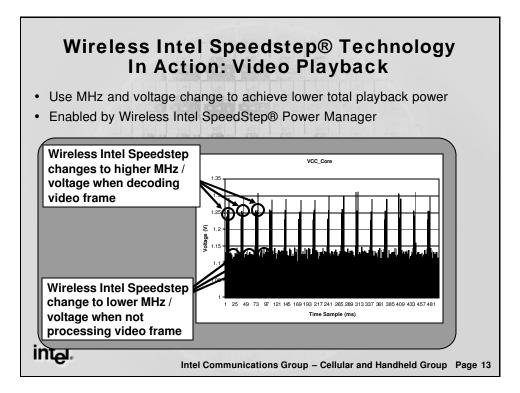




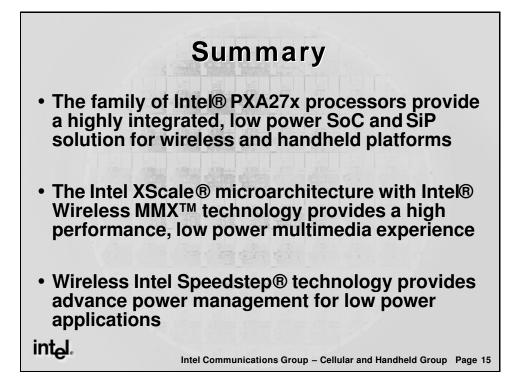












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