

Message from the Program Co-Chairs of Hot Chips 8

The continuing growth in chip capacity provides unparalleled opportunities for creative expression in the marketplace. In twenty years, we have seen single “logic” chips expand from 10K to 10M transistors and standard microprocessor performance increase by nearly 1000 times. The growth of multimedia, graphics, and scientific/technical performance is ever more dramatic and accelerating. Chips are hot!

This year's Hot Chips 8 conference presents some of the latest state-of-the-art in chip architectures, implementations, and related software. As many of the new developments are occurring in the multimedia and 3D graphics fields, half of this year's conference covers developments in these hot areas. The balance of the conference covers processor and cache/memory architecture, embedded processors, compilers, and unconventional uses of silicon.

Each year, the goal is to attract to Hot Chips an ensemble of the most interesting and topical presenters, panelists, tutorials, and speakers. We thank everyone who submitted an abstract for consideration to this year's conference.

As program co-chairs, we gratefully acknowledge our fellow Program Committee members for soliciting, reviewing, selecting submissions, and then organizing the sessions and reviewing the presentations. We also thank the Tutorial Chair, Organizing Committee and General Chair for making it all possible.

We also especially wish to thank the speakers and panelists for allocating time to prepare and present at this year's Hot Chips. Everyone's efforts have set the stage for a great Hot Chips 8 conference which we hope you will enjoy!

Robert Garner and Winfried Wilcke
Program Committee Co-Chairs
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