

Message from the General Chair of HOT Chips IV

The HOT Chips Symposium came about from a desire, soon after I began chairing the TCMM, that the TC become the sole sponsor of a new conference, and Bob Stewart's desire that the new conference be about "hot chips." The name stuck, and the place and time were right.

In the second year at Santa Clara University, attendance grew, and the third year, at the beautiful Stanford University campus, was extremely successful. We have always favored a reasonable fee in general, and very reasonable student fees in particular. The site is close to several universities that have IEEE student branches, and I am currently the IEEE Student Branch Counselor at UC Santa Cruz.

An emerging tradition has been the successful use of two program co-chairs. Dave Patterson and John Mashey, supported by Bob Miller, lead the team of David Ditzel, Ruby Lee, Teresa Meng and Uri Weiser in the technical content of the program, which is why you are all here. This year, the program is particularly exciting. Myself and others who have been involved in the organization, play an important yet supporting role. Bob Stewart devotes himself to two critical roles: handling both the registration and the local arrangements. Martin Freeman competently monitors the budget, and Andre Goforth, Elaine Grimes, and Doug Marquardt handle Publicity. Nam Ling receives, puts together, and arranges for printing this document handed out at the conference. Bob Miller has been excellent as an interface between the program committee and the organizing committee.

Design tools, the processor design learning curve helped along by Patterson, Hennessy, and others, plus digital technologies of higher density combined with lower power logic open new opportunities for "hot chips." The HOT Chips IV program covers the latest processors and features as well as some new specialty chips for the high performance arena.

Last year's introduction of an evening panel session is continued. This year introduces an optional Sunday tutorial for those who arrive early. A review of the concepts covered in the classic book on computer architecture by Hennessy and Patterson is an excellent way to prepare for the technical aspects of the major part of the symposium.

A special welcome to our international visitors. I received many e-mail inquiries about the program and registration from Europe and the Far East.

Glen Langdon
General Chair
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