## The Nitro Project – Next Generation AWS Infrastructure

Sr. Principal Engineer, EC2 Nitro, AWS Anthony Liguori <aliguori@amazon.com>







Nitro Overview

**Evolution of Nitro** 

Nitro Security Chip Deep Dive

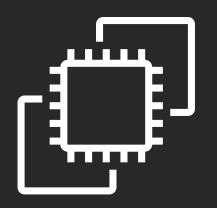
**AWS** Outposts

© 2018, Amazon Web Services, Inc. or its affiliates. All rights reserved.



After ten years of Amazon Elastic Compute Cloud (Amazon EC2), if we applied all of our learnings, what would a hypervisor look like?

## Nitro: Two years later



#### **AWS Nitro**

Launched in November 2017

In development since 2013

All new launches use Nitro

Purpose-built hardware/software

Hypervisor built for AWS





•	

<_start>:			
e9 59 e1 17	00	jmpq	ffff82d08037e15e
0f 1f 00		nopl	(%rax)
<multiboot1_header_< th=""><th>_start&gt;:</th><th></th><th></th></multiboot1_header_<>	_start>:		
02 b0 ad 1b	03 00	add	<pre>0x31bad(%rax),%dh</pre>
00 00		add	%al,(%rax)
fb		sti	
4f 52		rex.WR>	(B push %r10
e4 0f		in	\$0xf,%al
<multiboot1_header_< th=""><th>_end&gt;:</th><th></th><th></th></multiboot1_header_<>	_end>:		
0f 1f 40 00		nopl	0x0(%rax)
<multiboot2_header_< th=""><th>start&gt;:</th><th></th><th></th></multiboot2_header_<>	start>:		
d6		(bad)	
50		push	%rax
52			%rdx
e8 00 00 00	00		ffff82d080200020
88 00		mov	%al,(%rax)

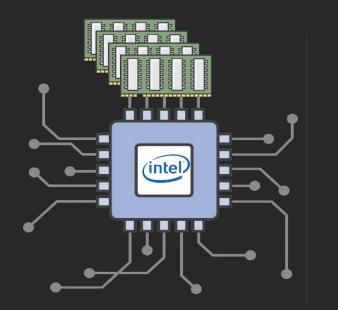


<u>\_\_\_\_</u>

6

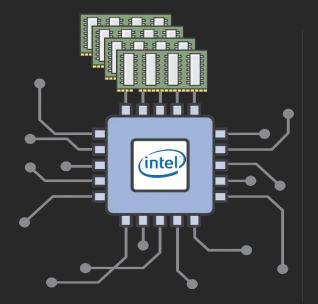
	e9 59 e1 17 00	jmpq	ffff82d08037e15e
	0f 1f 00	nopl	(%rax)
	<multiboot1_header_start>:</multiboot1_header_start>		
	02 b0 ad 1b 03 00	add	0x31bad(%rax),%dh
	00 00	add	%al,(%rax)
	fb	sti	
	4f 52	rex.WF	RXB push %r10
	e4 Of	in	\$0xf,%al
	<multiboot1_header_end>:</multiboot1_header_end>		
	0f 1f 40 00	nopl	0x0(%rax)
	<multiboot2_header_start>:</multiboot2_header_start>		
	d6	(bad)	
	50	push	%rax
	52	push	%rdx
☞ ┦┦┦┦	e8 00 00 00 00	callq	ffff82d080200020
	88 00	mov	%al,(%rax)





<_start>: e9 59 e1 17 0f 1f 00	00 jmpq nopl	
<pre><multiboot1_header 00="" 02="" 0f<="" 1b="" 4f="" 52="" ad="" b0="" e4="" fb="" pre=""></multiboot1_header></pre>	03 00 add add sti	
<pre><multiboot1_header 00<="" 0f="" 1f="" 40="" pre=""></multiboot1_header></pre>	_end>:	
<multiboot2_header d6 50 52 e8 00 00 00 88 00</multiboot2_header 	– (bad) push push	%rax %rdx   ffff82d080200020



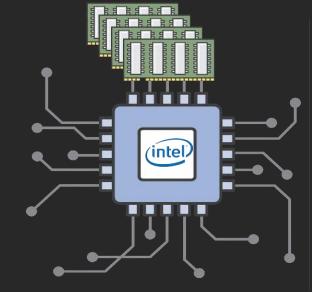


<_start>: e9 59 e1 17 00 0f 1f 00	jmpq nopl	ffff82d08037e15e (%rax)
<multiboot1_header_start>: 02 b0 ad 1b 03 00 00 00 fb 4f 52 e4 0f</multiboot1_header_start>	add add sti rex.WR in	0x31bad(%rax),%dh %al,(%rax) XB push %r10 \$0xf,%al
<multiboot1_header_end>: Of 1f 40 00</multiboot1_header_end>	nopl	0x0(%rax)
<multiboot2_header_start>: d6 50 52 e8 00 00 00 00 88 00</multiboot2_header_start>	(bad) push push callq mov	%rax %rdx ffff82d080200020 %al,(%rax)



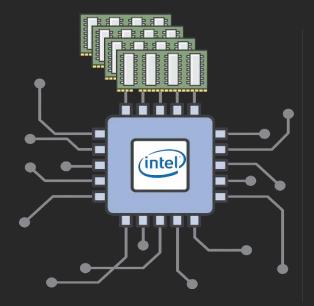


<_start>:			
e9 59 e1	. 17 00	jmpq	ffff82d08037e15e
0f 1f 00		nopl	(%rax)
<multiboot1_hea< td=""><td>der_start&gt;:</td><td></td><td></td></multiboot1_hea<>	der_start>:		
02 b0 ac	l 1b 03 00	add	0x31bad(%rax),%dh
00 00		add	%al,(%rax)
fb		sti	
4f 52		rex.WR)	KB push %r10
e4 0f		in	\$0xf,%al
<multiboot1_hea< td=""><td>der_end&gt;:</td><td></td><td></td></multiboot1_hea<>	der_end>:		
0f 1f 40	00	nopl	0x0(%rax)
<multiboot2_hea< td=""><td>der_start&gt;:</td><td></td><td></td></multiboot2_hea<>	der_start>:		
d6		(bad)	
50		push	%rax
52		push	%rdx
e8 00 00	00 00	callq	ffff82d080200020
88 00		mov	%al,(%rax)



© 2018, Amazon Web Services, Inc. or its affiliates. All rights reserved.





		<_start>:				
		e9 !	59 el 17	00	jmpq	ffff82d08037e15e
		0f 3	Lf 00		nopl	(%rax)
		<multiboot< th=""><th></th><th></th><th></th><th></th></multiboot<>				
	4	02 I	00 ad 1b	03 00	add	0x31bad(%rax),%dh
		00 (	90		add	%al,(%rax)
	ATE	fb			sti	
		4f !	52		rex.WR	XB push %r10
		e4 (	)f		in	\$0xf,%al
		<multiboot< th=""><th></th><th></th><th></th><th></th></multiboot<>				
		0f 3	Lf 40 00		nopl	0x0(%rax)
		<multiboot2< th=""><th>) header</th><th>ctarts</th><th></th><th></th></multiboot2<>	) header	ctarts		
\ /N / N /		d6			(bod)	
VMM					(bad)	
	J	50			push	%rax
		52			push	
		e8 (	00 00 00	00	callq	ffff82d080200020
		88 (	90		mov	%al,(%rax)



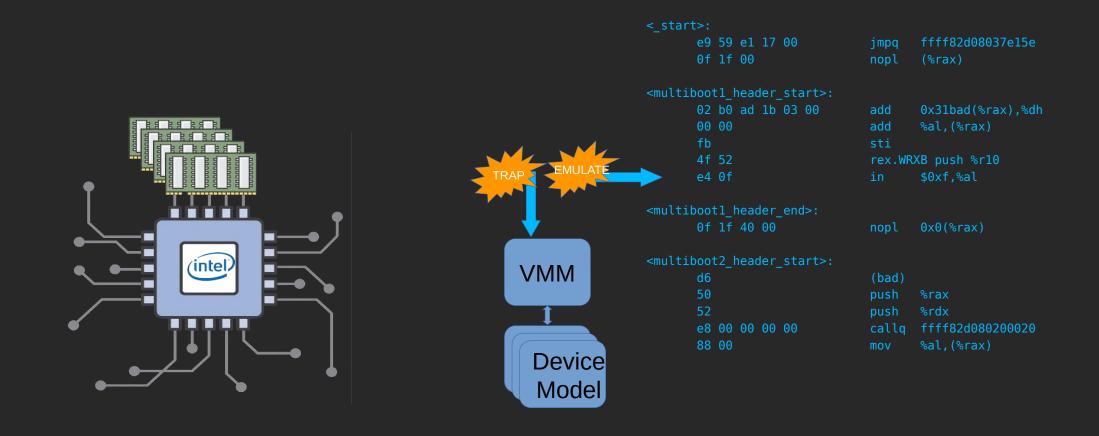




#### What happened?

- The VMM is the heart of a hypervisor.
- As long as a statistical majority of instructions execute natively, we call this virtualization.
- Not all emulation can be handled by the VMM.







#### What happened?

- A hypervisor consists of:
- - Virtual Machine Monitor
- Many device models (10 to 100s)
- - Scheduler, memory manager, etc.
- This was state of the art in 1974
- Not all of the assumptions held true though...



#### From 1974 to 2006

- Early Intel processors did not trap
- The Xen project found a clever solution
- Paravirtualization modifies the OS to trap
- Hypercalls directly invoke the VMM
- EC2 launched using Xen Paravirtualization

< start>:		
e9 59 e1 17 00	impa	ffff8
0f 1f 00		(%rax
<multiboot1_header_start>:</multiboot1_header_start>		
02 b0 ad 1b 03 00	add	0x31b
00 00	add	%al,(
fb	sti	
4f 52	rex.WR	XB pus
e4 Of	HYPERC	ALL io
<multiboot1_header_end>:</multiboot1_header_end>		
0f 1f 40 00	nopl	0x0(%
<multiboot2_header_start>:</multiboot2_header_start>		
 d6	(bad)	
50	push	%rax
52	push	
e8 00 00 00 00	callq	ffff8
88 00	mov	%al,(

32d08037e15e <)

oad(%rax),%dh [%rax)

sh %r10 o\_in

¦rax)

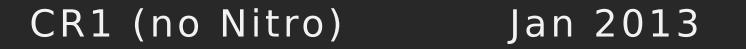
82d080200020 (%rax)

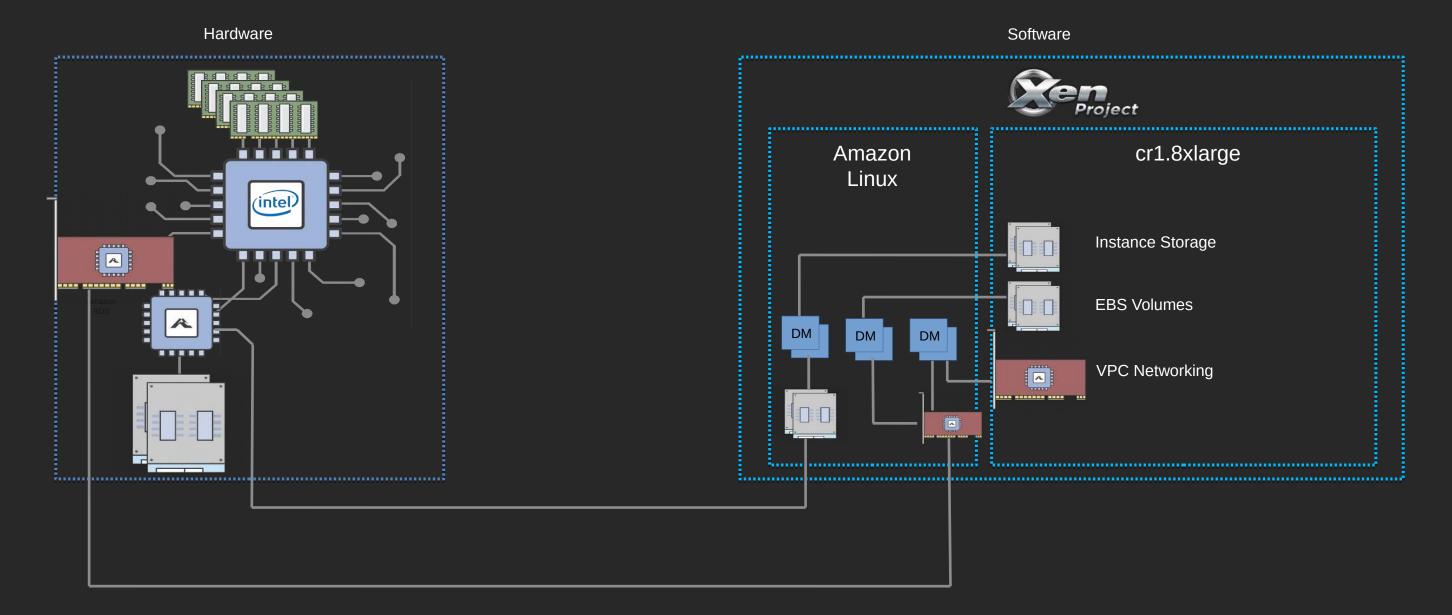


### Evolution of the Nitro System

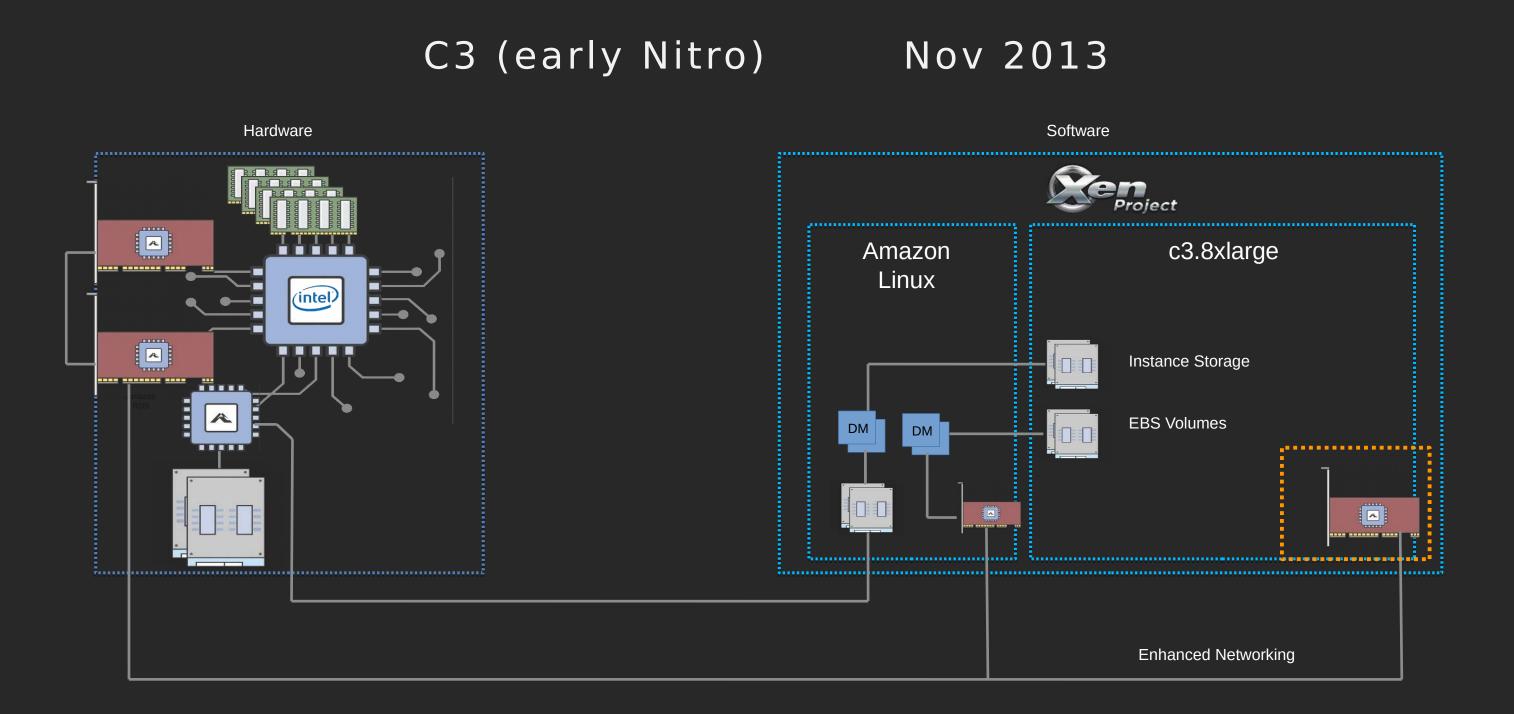
© 2018, Amazon Web Services, Inc. or its affiliates. All rights reserved.





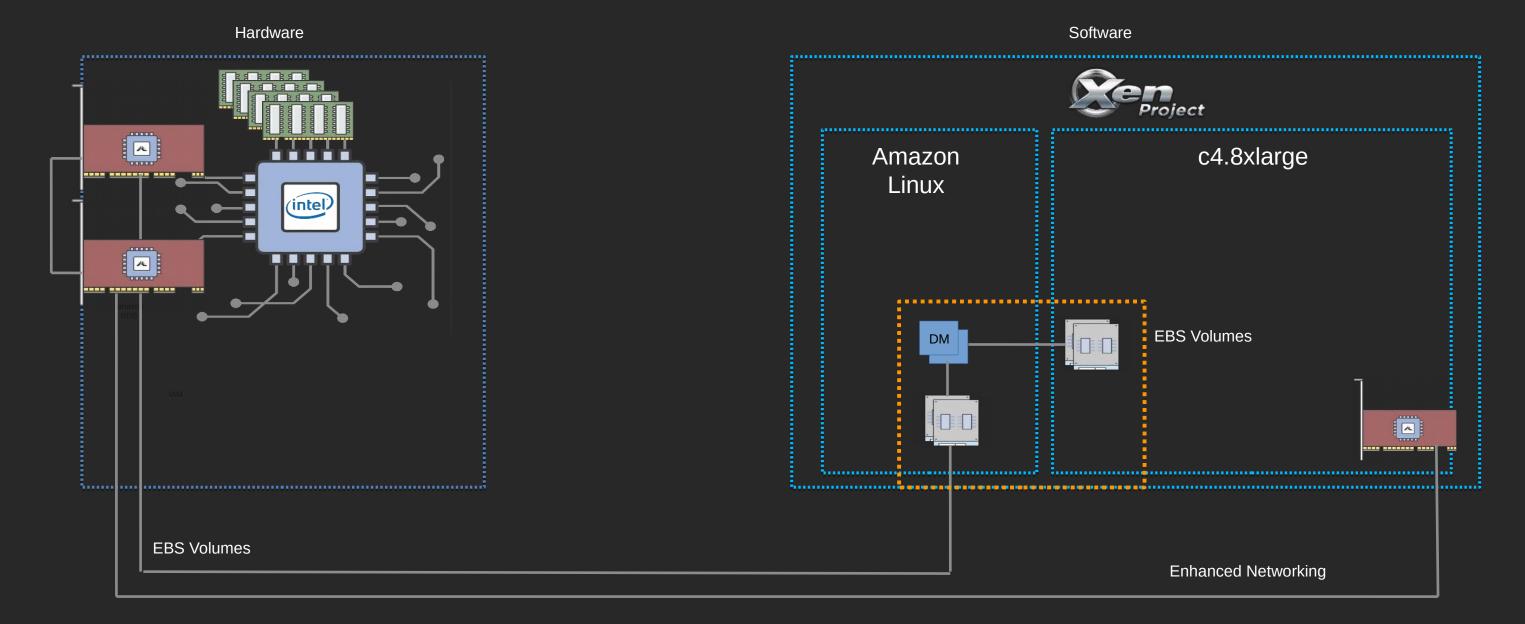








C4

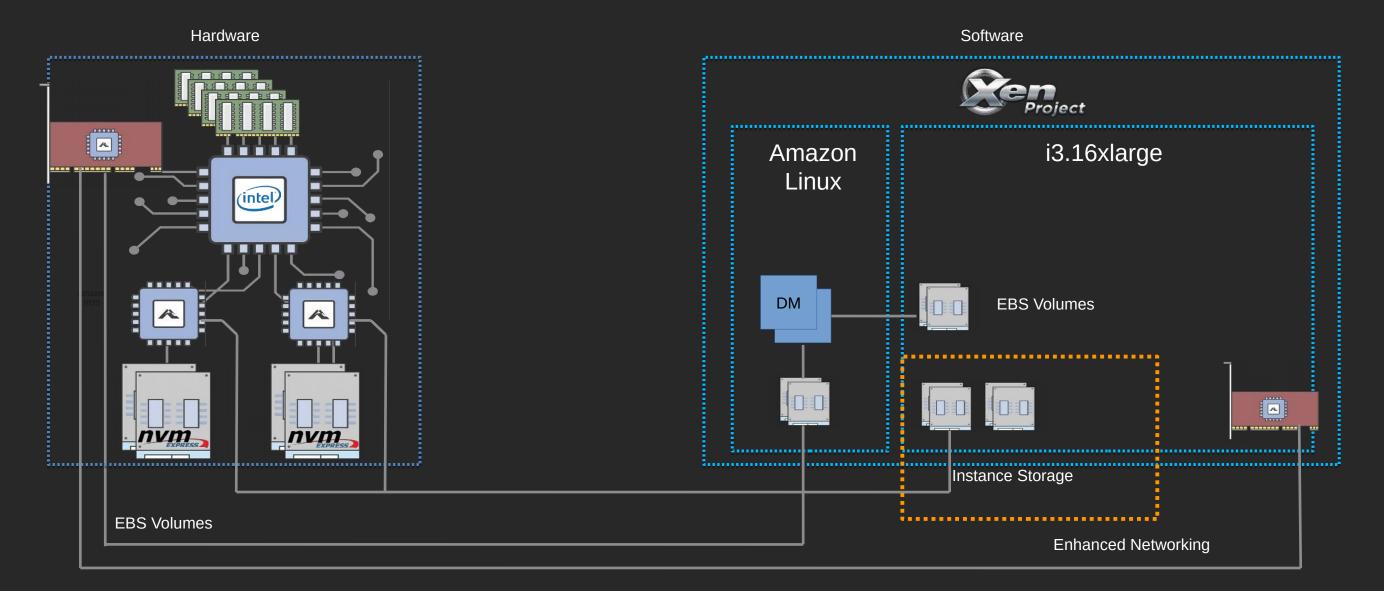


#### Jan 2015



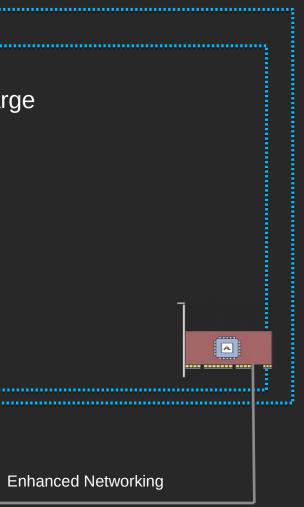
13

Feb 2017





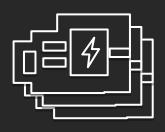
#### Nov 2017 C5 Hardware Software Nitro Hypervisor a a a a a a c5.18xlarge \*\*\*\* 00000 .... intel 3...... ...... EBS Volumes





# Nitro in three parts

#### Nitro Cards



**VPC** Networking Amazon Elastic Block Store (Amazon EBS) Instance Storage System Controller

#### Nitro Security Chip



Integrated into motherboard Protects hardware resources Hardware Root of Trust



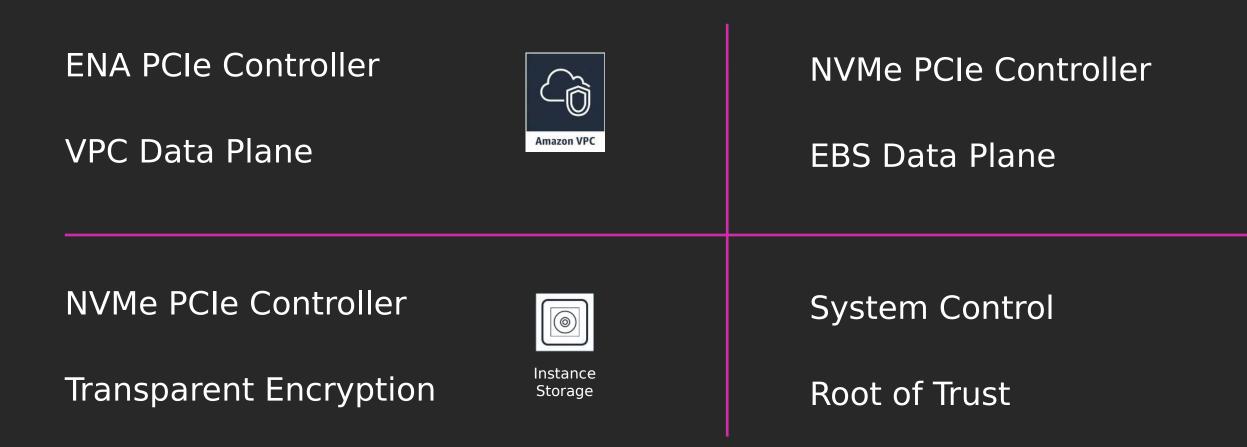
Lightweight hypervisor Memory and CPU allocation Bare Metal-like performance

#### Nitro Hypervisor



















# Nitro Card for VPC



### **ENA** Controller

Drivers available for all major operating systems Independent of fabric

### **VPC** Data Plane

Encapsulation Security Groups Limiters Routing





# Nitro Card for EBS



NVMe Controller Standard drivers broadly available

EBS Data Plane Encryption support NVM to remote storage protocol





# Nitro Card for Instance Storage



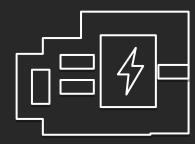
Instance Storage NVMe Controller Standard drivers broadly available

Instance Storage Data Plane Transparent Encryption Limiters Drive monitoring





# Nitro Card Controller



Nitro Controller

#### System Control

Provides passive API endpoint Coordinates all other Nitro Cards Coordinates with Nitro Hypervisor Coordinates with Nitro Security Chip

### Hardware Root of Trust Provides measurement and attestation





# Nitro Security Chip

Custom microcontroller that traps all I/O to nonvolatile storage



Controllable from the Nitro Controller to hold system boot

Provides a simple, hardware-based root of trust





Boot starts untrusted and must prove that system is trustworthy.

Deep complexity with millions of lines of code.

Unavoidable complexity due to need to support legacy and general purpose workloads.





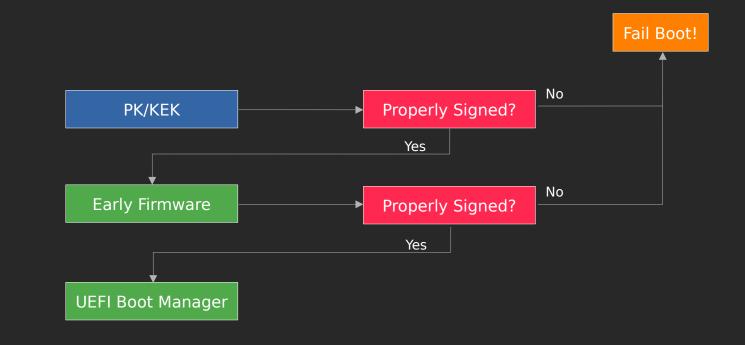




Boot starts untrusted and must prove that system is trustworthy.

Deep complexity with millions of lines of code.

Unavoidable complexity due to need to support legacy and general purpose workloads.



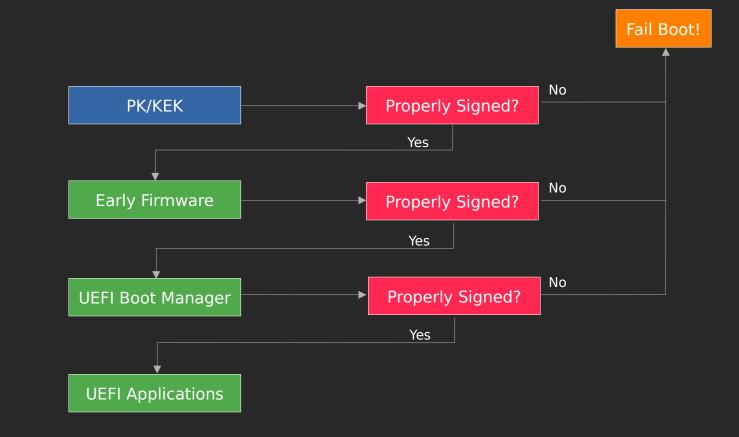




Boot starts untrusted and must prove that system is trustworthy.

Deep complexity with millions of lines of code.

Unavoidable complexity due to need to support legacy and general purpose workloads.



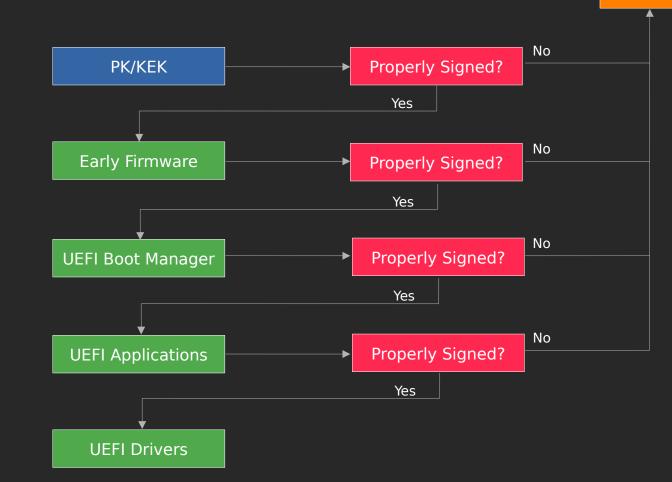




Boot starts untrusted and must prove that system is trustworthy.

Deep complexity with millions of lines of code.

Unavoidable complexity due to need to support legacy and general purpose workloads.





#### Fail Boot!



Boot starts untrusted and must prove that system is trustworthy.

Deep complexity with millions of lines of code.

Unavoidable complexity due to need to support legacy and general purpose workloads.





#### Fail Boot!

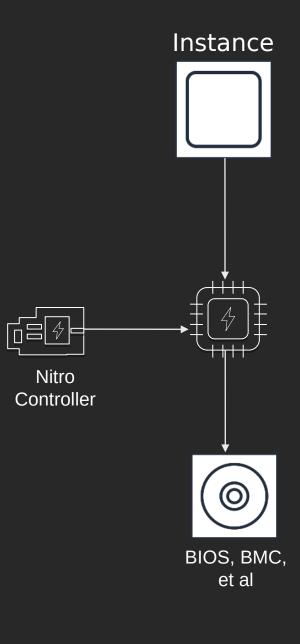


# Nitro Hardware Root of Trust

Radical simplification enabled by Nitro Cards.

All write access to non-volatile storage is blocked in hardware.

Simple to understand security due to lack of legacy.





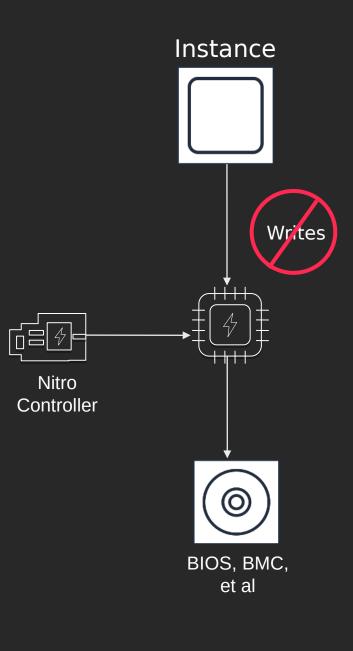


# Nitro Hardware Root of Trust

Radical simplification enabled by Nitro Cards.

All write access to non-volatile storage is blocked in hardware.

Simple to understand security due to lack of legacy.







# Nitro Hypervisor

**KVM-based** hypervisor with custom MM and small userspace



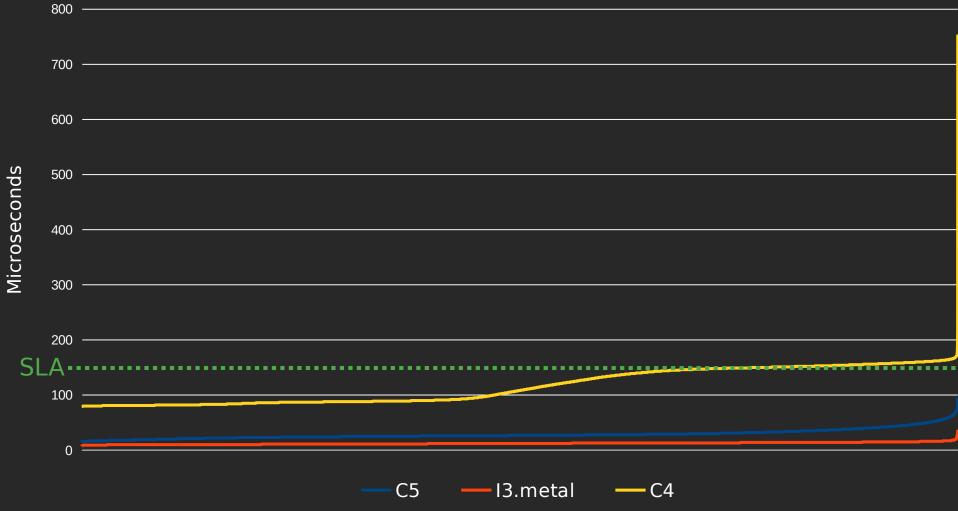
Only executes on behalf of instance, quiescent.

With Nitro, the hypervisor can be fast and simple



# Nitro Hypervisor Jitter

#### Wake up Delay



© 2018, Amazon Web Services, Inc. or its affiliates. All rights reserved.







### What comes next?

© 2018, Amazon Web Services, Inc. or its affiliates. All rights reserved.



# Nitro: Anywhere you need it



Nitro hardware and software in your data center

Access via standard AWS API and console

#### AWS Outposts

Deploy apps to Outposts using AWS services



