

# Welcome to Hot Chips 30!

Organizing Committee
John D. Davis, Vice Chair





- Registration
  - Record numbers!!!
- Sponsorship
  - 4 Platinum
  - 4 Gold
  - 11 Silver
- Volunteer-run conference
  - Steering Committee
  - Organizing Committee
  - Program Committee



### Parking

- Must use the Monday+Tuesday pass
  - Not the Sunday pass
- Park only on floors 2 and higher for the structure
- •Don't have a pass?
  - We have spare passes in the lobby



#### Presentations Available....

- Download individual or all presentations
  - •http://server.hotchips.event



## Hot Chips Then and Now



#### Locations

• HC1: Stanford: Kresge Auditorium → Demolished 2009

HC10: Stanford: Memorial Auditorium

- HC20: Stanford: Memorial Auditorium
  - Until HC23, again @ HC25 ....

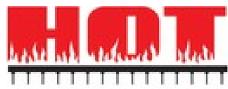
- HC30: De Anza College: Flint Center for the Performing Arts
  - HC24, HC26-30



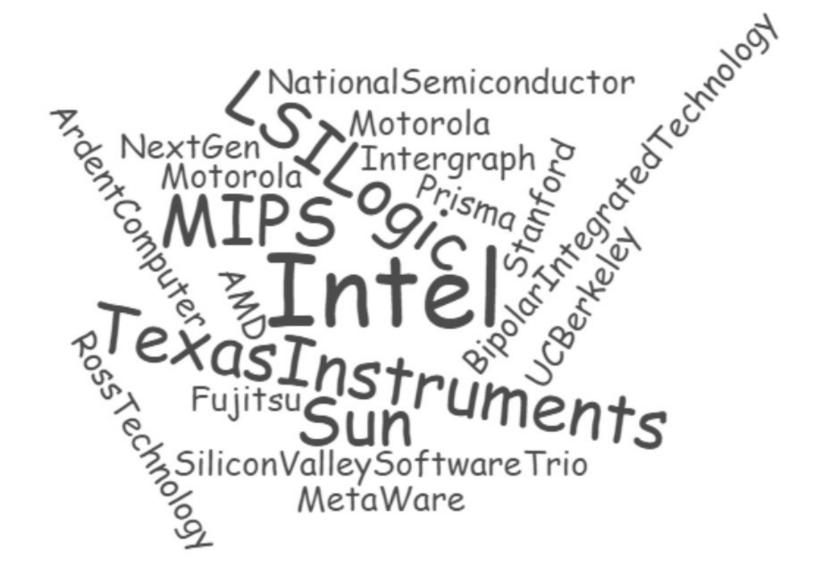
#### Sessions Then and Now

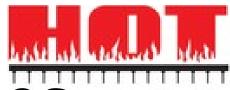
- HC1
  - New SPARC CPUs
  - RISC CPU Updates
  - New Processor Architecture
  - Floating Point Processors
  - New CISC CPUs
  - Embedded CPUs
  - Graphics Coprocessors

- HC30
  - Mobile/Power Efficient Processors
  - Graphics Solutions
  - IoT/Edge Computing
  - Security
  - Switching Fabrics and FPGA Architectures
  - New Technologies
  - Machine Learning I
  - Machine Learning II
  - Server Processors



## HC 1 Companies & Universities !!



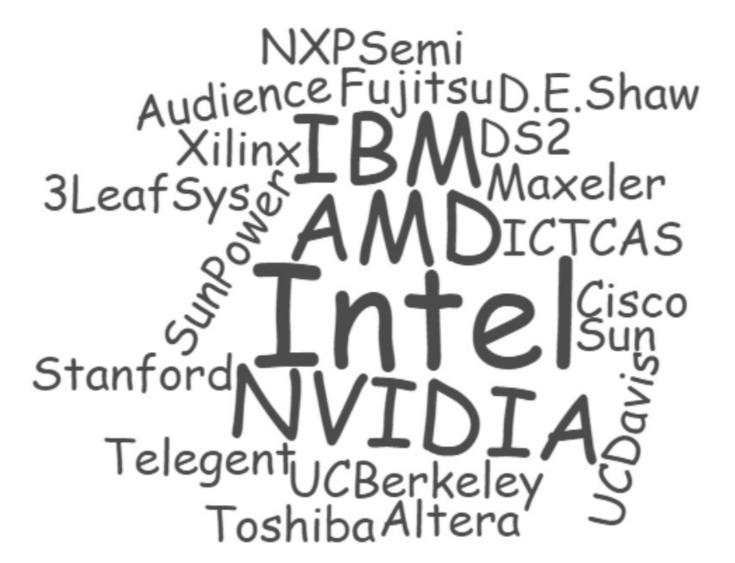


## HC 10 Companies & Universities Property Property

E-muSystems Motorola StellarSemiconductor PhilipsResearchLabs SandCraft



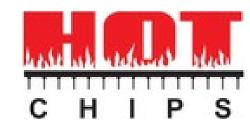
## HC 20 Companies & Universities !





## HC 30 Companies & Universities 1 P



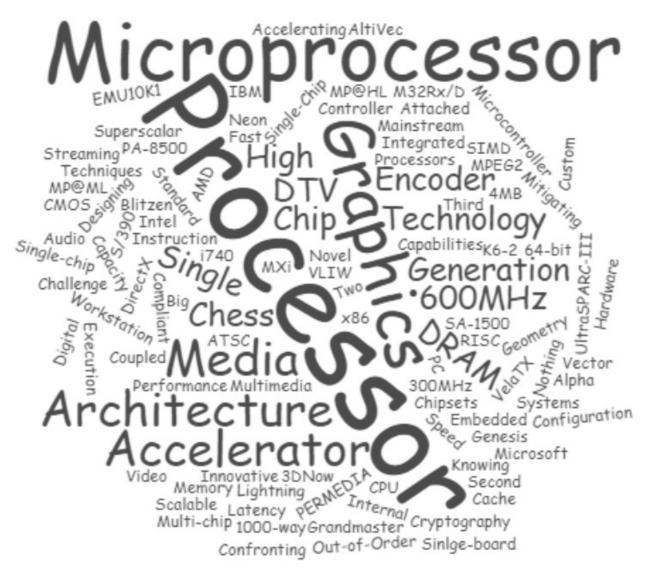


#### HC 1 Presentations





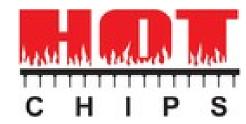












#### HC 30 Presentations

